

10825904\_CLS1.txt  
Most Frequently Occurring Classifications of Patents Returned  
From A Search of 10825904 on June 12, 2006

Original Classifications

5 365/185.11  
3 710/5  
2 365/185.33  
2 703/28  
2 709/221  
2 710/100  
2 710/263  
2 710/301  
2 710/309  
2 714/6  
2 715/754  
2 717/124  
2 717/178

Cross-Reference Classifications

7 365/230.03  
4 365/218  
3 365/185.29  
3 365/185.33  
3 707/100  
3 713/100  
3 713/155  
3 713/2  
3 715/507  
3 718/100  
2 345/531  
2 365/185.12  
2 365/189.04  
2 370/402  
2 375/222  
2 379/110.01  
2 700/17  
2 703/23  
2 703/27  
2 709/208  
2 709/220  
2 709/222  
2 709/224  
2 709/227  
2 709/229  
2 710/1  
2 710/10  
2 710/266  
2 710/311  
2 710/62  
2 710/8  
2 711/115  
2 711/117  
2 713/1  
2 713/320  
2 713/321  
2 714/38  
2 717/178

710/8

34

346

710/8-19

34 - 38

40 - 46

62 - 64

72 - 74

316 -

Combined Classifications

7 365/230.03  
5 365/185.11  
5 365/185.33

4 365/218  
4 710/5  
4 713/2  
4 717/178  
3 365/185.29  
3 703/23  
3 703/27  
3 703/28  
3 707/100  
3 709/208  
3 709/220  
3 710/10  
3 710/311  
3 713/100  
3 713/155  
3 713/320  
3 714/38  
3 715/507  
3 718/100  
2 345/520  
2 345/531  
2 345/537  
2 365/185.12  
2 365/189.04  
2 370/389  
2 370/402  
2 375/222  
2 379/110.01  
2 379/88.22  
2 700/17  
2 703/24  
2 705/55  
2 707/1  
2 707/10  
2 707/104.1  
2 707/203  
2 707/205  
2 709/201  
2 709/217  
2 709/221  
2 709/222  
2 709/224  
2 709/227  
2 709/229  
2 709/230  
2 710/1  
2 710/100  
2 710/104  
2 710/260  
2 710/263  
2 710/266  
2 710/301  
2 710/309  
2 710/62  
2 710/72  
2 710/8  
2 711/103  
2 711/112  
2 711/113  
2 711/115  
2 711/117  
2 711/118  
2 711/164

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2 712/28  
2 713/1  
2 713/321  
2 713/322  
2 713/324  
2 714/6  
2 715/754  
2 717/124  
2 717/136



- ☒ Drafts
  - ☒ IS&R:
- ☐ Pending
- ☒ Active
  - ☒ L2: (107858) (during while) near5 (configur\$5 reset\$5 initializ\$5)
  - ☒ L3: (2717) (port pin) near9 2
  - ☒ L4: (22) disable near5 3
  - ☒ L5: (77018) (during while) near5 (configur\$5 initializ\$5)
  - ☒ L6: (1982) disable near5 port
  - ☒ L8: (6) 5 with 6
  - ☒ L9: (38746) ((input near1 output) near2 port)
  - ☒ L10: (34) 5 with 9
  - ☒ L11: (4) (input near1 output) near9 disable near9 5
  - ☒ L12: (512) disabl\$3 near9 5
  - ☒ L13: (58) 12 near9 ( device unit peripheral)
  - ☒ L14: (629) disable near5 keyboard
  - ☒ L15: (7) 5 with 14
  - ☒ L16: (25670) (710/\$3).CCLS.
  - ☒ L17: (3407) 5 and 16
  - ☒ L18: (258) 16 and 6
  - ☒ L19: (58) 12 and 16
- ☐ Failed
- ☒ Saved
  - ☒ S10: (83) initializ\$5 near9 disabl\$5 near9 port
  - ☒ S11: (257) during near9 disabl\$5 near9 port
  - ☒ S12: (42723) (during while) near9 input near9 output
  - ☒ S13: (2791292) (reset initializ\$5 configur\$5) wht S12
  - ☒ S14: (2531) (reset initializ\$5 configur\$5) with S12
  - ☒ S15: (21) disable with S14
  - ☒ S16: (557) (reset initializ\$5 configur\$5) near9 disable near9 (during while)
- ☐ Favorites
- ☒ Tagged (9)
- ☐ UDC
- ☐ Queue
- ☐ Trash

      ☒ PluralsDefault operator: ☒ OR ☐ Highlight all hit terms initially

12 and 16

☒ BRS form ☒ IS&R form ☒ Image ☒ Text

	U		Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cla	
1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6963942 B2	20051108	5	High availability system and method for improved initializatio	710/108	710/10;		Benyu
2	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6457069 B1	20020924	29	Method and apparatus for providing support for dynamic r	710/8	713/100;		Stanle
3	<input type="checkbox"/>	<input type="checkbox"/>	US 20060123161	20060608	11	Methods and systems for buffering console port data in a	710/52	326/105;		L. Wall
4	<input type="checkbox"/>	<input type="checkbox"/>	US 20060004928	20060105	14	Configurable feature selection mechanism	710/8	710/10;		Hess; C
5	<input type="checkbox"/>	<input type="checkbox"/>	US 20050262376	20051124	41	Method and apparatus for busse	713/600	710/107		McBai
6	<input type="checkbox"/>	<input type="checkbox"/>	US 20050165970	20050728	32	Adaptive-allocation of I/O bandwidth using a configurable	710/1			Ching
7	<input type="checkbox"/>	<input type="checkbox"/>	US 20050091427	20050428	22	Integrated circuit device having send/receive macro for	710/48	710/260		Yoshid
8	<input type="checkbox"/>	<input type="checkbox"/>	US 20050066099	20050324	9	Interrupt disabling apparatus, system and method	710/262			Radhc
9	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20050010904	20050113		Information recording device, method for the same program	717/120	710/74		Moriok
10	<input type="checkbox"/>	<input type="checkbox"/>	US 20040243725	20041202	18	Servicing multiple hot-plug events utilizing a common even	710/1			Yakov
11	<input type="checkbox"/>	<input type="checkbox"/>	US 20040054841	20040318	19	Method and apparatus for promoting memory read comm	710/310			Calliso
12	<input type="checkbox"/>	<input type="checkbox"/>	US 20030167366	20030904	9	Interrupt disabling apparatus, system and method	710/262			Radhc
13	<input type="checkbox"/>	<input type="checkbox"/>	US 20030040897	20030227	129	Man machine interface for power management control sys	703/18	709/220;		Murph
14	<input type="checkbox"/>	<input type="checkbox"/>	US 20020169911	20021114	18	Method and apparatus for providing extended functionalit	710/300	710/9		Shelto
15	<input type="checkbox"/>	<input type="checkbox"/>	US 20020023179	20020221	29	METHOD AND APPARATUS FOR	710/8			STANI

☒ Hits ☐ Details ☒ HTML

EAST - [10674146.wsp:1]

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DBs US-PGPUB ☒ Plurals

Default operator: ☐ OR ☐ Highlight all hit terms initially

**Drafts**

- IS&R:
- Pending

**Active**

- L2: (107858) (during while) near5 (configur\$5 reset\$5 initializ\$5)
- L3: (2717) (port pin) near9 2
- L4: (22) disable near5 3
- L5: (77018) (during while) near5 (configur\$5 initializ\$5)
- L6: (1982) disable near5 port
- L8: (6) 5 with 6
- L9: (38746) ((input near1 output) near2 port)
- L10: (34) 5 with 9
- L11: (4) (input near1 output) near9 disable near9 5
- L12: (512) disabl\$3 near9 5
- L13: (58) 12 near9 ( device unit peripheral)
- L14: (629) disable near5 keyboard
- L15: (7) 5 with 14
- L16: (25670) (710/\$3).CCLS.
- L17: (3407) 5 and 16
- L18: (258) 16 and 6
- L19: (58) 12 and 16

**Failed**

**Saved**

- S10: (83) initializ\$5 near9 disabl\$5 near9 port
- S11: (257) during near9 disabl\$5 near9 port
- S12: (42723) (during while) near9 input near9 output
- S13: (2791292) (reset initializ\$5 configur\$5) wiht S12
- S14: (2531) (reset initializ\$5 configur\$5) with S12
- S15: (21) disable with S14
- S16: (557) (reset initializ\$5 configur\$5) near9 disable near9 (during while)

**Favorites**

**Tagged (9)**

UDC

Queue

**Trash**

BRS form IS&R form Image Text

	U		Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cla	
1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5497379 A	19960305	41	Method and apparatus for	714/731	324/158.1;		Whets
2	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 7020764 B2	20060328	25	Semiconductor processing	712/37	326/38;		Kubot
3	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6397301 B1	20020528	18	Preventing access to secure	711/138	711/163		Quaci
4	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20010052038	20011213	29	Data storewidth accelerator	710/68	710/22;		Fallon,
5	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20030105994	20030605	6	High availability system and	714/43			Benyu
6	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6457069 B1	20020924	29	Method and apparatus for	710/8	326/105;		Stanle
7	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6963942 B2	20051108	5	High availability system and	710/108	710/10;		Benyu
8	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20030052710	20030320	11	Methods and apparatus for	326/37			Lassele
9	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6631437 B1	20031007	17	Method and apparatus for	710/313	710/107;		Colliso

Hits Details HTML

Ready

NUM



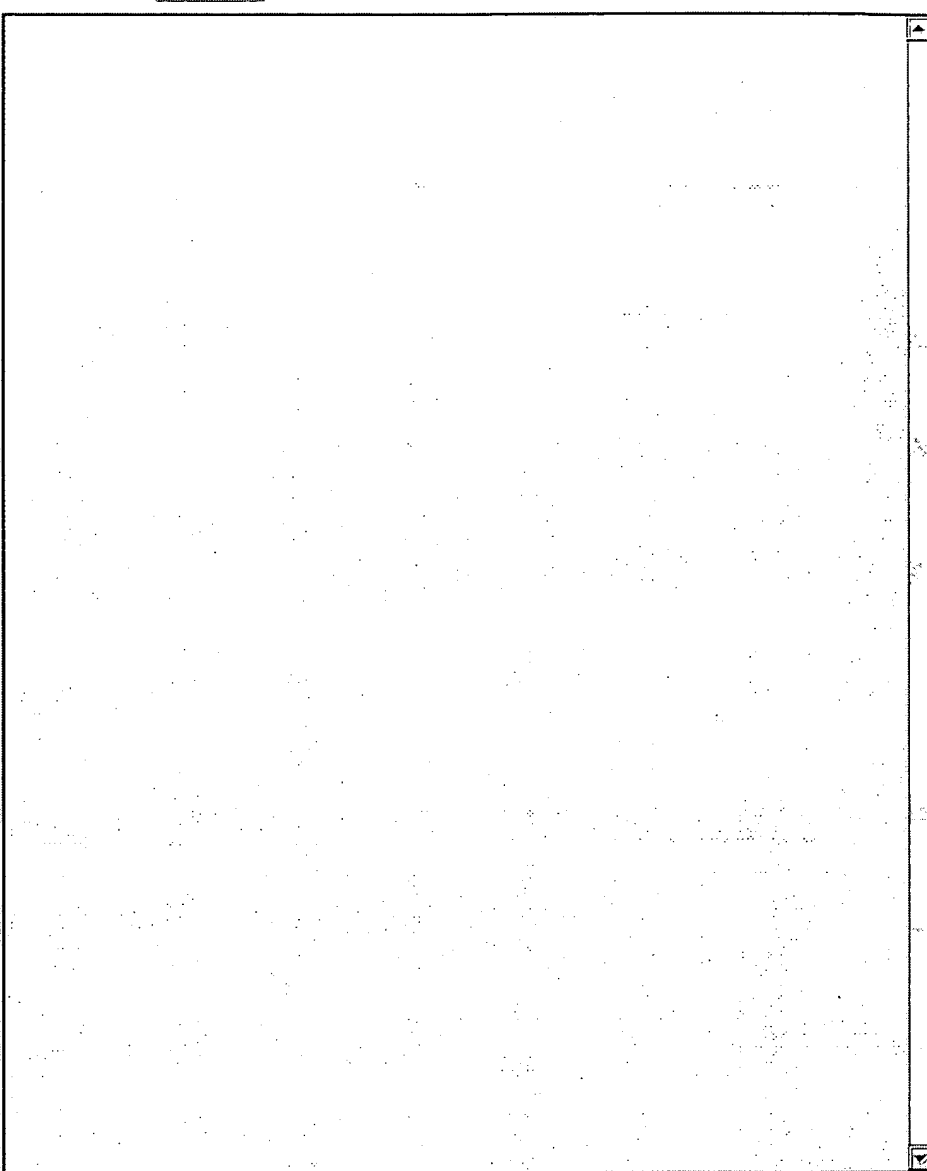
- [-] Drafts
  - [-] IS&R:
- [-] Pending
- [-] Active
  - L2: (107858) (during while) near5 (configur\$5
  - L3: (2717) (port pin) near9 2
  - L4: (22) disable near5 3
  - L5: (77018) (during while) near5 (configur\$5
  - L6: (1982) disable near5 port
  - L8: (6) 5 with 6
  - L9: (38746) ((input near1 output) near2 port
  - L10: (34) 5 with 9
  - L11: (4) (input near1 output) near9 disable n
  - L12: (512) disabl\$3 near9 5
  - L13: (58) 12 near9 ( device unit peripheral)
  - L14: (629) disable near5 keyboard
  - L15: (7) 5 with 14
  - L16: (25670) (710/\$3).CCLS.
  - L17: (3407) 5 and 16
  - L18: (258) 16 and 6
  - L19: (58) 12 and 16
- [-] Failed
- [-] Saved
  - S10: (83) initializ\$5 near9 disabl\$5 near9 port
  - S11: (257) during near9 disabl\$5 near9 port
  - S12: (42723) (during while) near9 input near5
  - S13: (2791292) (reset initializ\$5 configur\$5) w
  - S14: (2531) (reset initializ\$5 configur\$5) with S
  - S15: (21) disable with S14
  - S16: (557) (reset initializ\$5 configur\$5) near9
- [-] Favorites
- [-] Tagged (9)
- [-] UDC
- [-] Queue
- [-] Trash






 US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM\_TDB

☒ Plurals

 Default operator: 
☐ Highlight all hit terms initially







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1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5497379 A	19960305	41	Method and apparatus for	714/731	324/158.1;		Whets
2	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 7020764 B2	20060328	25	Semiconductor processing	712/37	326/38;		Kubot
3	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6397301 B1	20020528	18	Preventing access to secure	711/138	711/163		Quact
4	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20010052038	20011213	29	Data storewidth accelerator	710/68	710/22;		Fallon,
5	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20030105994	20030605	6	High availability system and	714/43			Benyu
6	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6457069 B1	20020924	29	Method and apparatus for	710/8	326/105;		Stanle
7	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6963942 B2	20051108	5	High availability system and	710/108	710/10;		Benyu
8	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20030052710	20030320	11	Methods and apparatus for	326/37			Lassek
9	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6631437 B1	20031007	17	Method and apparatus for	710/313	710/107;		Colliso




Ready

NUM


☐ Drafts

☐ IS&R:

☐ Pending

☒ Active

- ☒ L1: (91144) (initializ\$5 configur\$5) near9 (disabl\$3 enabl\$3)
- ☒ L2: (40181) port near9 (disabl\$3 enabl\$3)
- ☒ L3: (1505) 1 near9 2
- ☒ L4: (3323) (during while) near9 1
- ☒ L5: (25) 3 near9 4
- ☒ L6: (355873) device near9 (disabl\$3 enabl\$3)
- ☒ L7: (10402) (during while) near9 6
- ☒ L8: (314) 1 near9 7

☐ Failed

☒ Saved

- ☒ S10: (83) initializ\$5 near9 disabl\$5 near9 port
- ☒ S11: (257) during near9 disabl\$5 near9 port
- ☒ S12: (42723) (during while) near9 input near9 output
- ☒ S13: (2791292) (reset initializ\$5 configur\$5) with S12
- ☒ S14: (2531) (reset initializ\$5 configur\$5) with S12
- ☒ S15: (21) disable with S14
- ☒ S16: (557) (reset initializ\$5 configur\$5) near9 disable near9 (during while)
- ☒ S17: (107858) (during while) near5 (configur\$5 reset\$5 initializ\$5)
- ☒ S18: (2717) (port pin) near9 S17
- ☒ S19: (22) disable near5 S18
- ☒ S20: (77018) (during while) near5 (configur\$5 initializ\$5)
- ☒ S21: (1982) disable near5 port
- ☒ S23: (6) S20 with S21
- ☒ S24: (38746) ((input near1 output) near2 port)
- ☒ S25: (34) S20 with S24
- ☒ S26: (4) (input near1 output) near9 disable near9 S20
- ☒ S27: (512) disabl\$3 near9 S20
- ☒ S28: (58) S27 near9 ( device unit peripheral)
- ☒ S29: (629) disable near5 keyboard
- ☒ S30: (7) S20 with S29
- ☒ S31: (25670) (710/\$3).CCLS.
- ☒ S32: (3407) S20 and S31
- ☒ S33: (258) S31 and S21
- ☒ S34: (58) S27 and S31

☐ Favorites

☐ Tagged (9)

☐ UDC

☐ Queue

☐ Trash

    
  ☒ Plurals

 Default operator:  ☐ Highlight all hit terms initially

1 near9 7

☒ BRS form ☒ IS&R form ☒ Image ☒ Text ☒ HTML

	U		Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cla	
1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6457069 B1	20020924	29	Method and apparatus for	710/8	326/105;		Star
2	<input type="checkbox"/>	<input type="checkbox"/>	US 20060126401	20060615	14	Reducing DQ pin capacitance	365/189.05	710/110;		Ba;
3	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20060109106	20060525		Shipping container monitoring	340/539.13	340/545.6		Brau
4	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20060107194	20060518	21	System and method for virtual	715/500	715/501.1;		Gak
5	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20060081697	20060420		Ensuring the performance of	235/377	340/539.13;		Brnt
6	<input type="checkbox"/>	<input type="checkbox"/>	US 20060075313	20060406	13	Read control systems and	714/56	702/182		Car
7	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20060042339	20060302	20	Manufacturing device,	72/15.1			Der
8	<input type="checkbox"/>	<input type="checkbox"/>	US 20060041767	20060223	15	Methods, devices and	713/323			Max
9	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20060035670	20060216		Distributed architecture wireless	455/557			Sou
10	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20060034284	20060216		Apparatus and system for	370/392			Saki
11	<input type="checkbox"/>	<input type="checkbox"/>	US 20060034192	20060216	15	Apparatus and system for	370/254	370/403		Hyt

☒ Hits ☒ Details ☒ HTML